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APPLICATION NUMBER	FILING or 371(c) DATE	GRP ART UNIT	FIL FEE REC'D	ATTY.DOCKET.NO	TOT CLAIMS	IND CLAIMS
10/721,039	11/21/2003	2814	1350	G0631.70038 US00	45	3

CONFIRMATION NO. 5026

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CORRECTED FILING RECEIPT

Date Mailed: 09/07/2007

Receipt is acknowledged of this non-provisional patent application. The application will be taken up for examination in due course. Applicant will be notified as to the results of the examination. Any correspondence concerning the application must include the following identification information: the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections**

Applicant(s)

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Assignment For Published Patent Application

Analog Devices, Inc., Norwood, MA

Power of Attorney: The patent practitioners associated with Customer Number 23628

Domestic Priority data as claimed by applicant

This appln claims benefit of 60/427,993 11/21/2002

Foreign Applications

If Required, Foreign Filing License Granted: 02/23/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/721,039**

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

INTEGRATED MOS ONE-WAY ISOLATION COUPLER AND A SEMICONDUCTOR CHIP HAVING AN INTEGRATED MOS ISOLATION ONE-WAY COUPLER LOCATED THEREON

Preliminary Class

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